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Application Notes

- AN 73 Implementing FIR Filters in FLEX Devices
- AN 84 Implementing fft with On-Chip RAM in FLEX 10K Devices
- AN 86 Implementing the pci_a Master/Target in FLEX 10K Devices
- AN 98 Comparing Performance of Common Megafunctions
- AN 99 Comparing Performance of Dual-Port Memory Functions
- AN 125 Evaluating AMPP & MegaCore Functions

Catalogs

- Intellectual Property Catalog
- LPM Quick Reference Guide

Customer Applications

- Bailey Controls Uses Megafunctions to Solve the PCI Challenge Customer Application

Data Sheets

- a16450 Universal Asynchronous Receiver/Transmitter Data Sheet
- a6402 Universal Asynchronous Receiver/Transmitter Data Sheet
- a6850 Asynchronous Communications Interface Adapter Data Sheet
- a8237 Programmable DMA Controller Data Sheet
- a8251 Programmable Communications Interface Data Sheet
- a8255 Programmable Peripheral Interface Adapter Data Sheet
- a8259 Programmable Interrupt Controller Data Sheet
- crc MegaCore Function Parameterized CRC Generator/Checker Data Sheet
- fft Fast Fourier Transform Data Sheet
- FLEX 10K PCI Prototype Board Data Sheet
- FLEX 10KE PCI Development Board Data Sheet
- PCI Master/Target MegaCore Function with DMA Data Sheet
- pci_b PCI Master/Target MegaCore Function Data Sheet

Megafunction Contents

- pcit1 PCI Target MegaCore Function Data Sheet
- RGB2YCrCb & YCrCb2RGB Color Space Converters Data Sheet
- SignalTap Embedded Logic Analyzer Megafunction Data Sheet

Functional Specifications

- FS 1 FIR Filters
- FS 2 fp_add_sub Floating-Point Adder/Subtractor
- FS 4 fp_mult Floating-Point Multiplier
- FS 5 round Data Word Rounder
- FS 6 saturate Data Word Saturator
- FS 7 fft_on_chip Fast Fourier Transform

General Information

- Introduction (to the Altera *1999 Data Book*)
- Ordering Information

Selector Guides

- Megafunctions Selector Guide

Solution Briefs

- SB 2 High-Speed Adaptive FIR Filter Megafunction
- SB 3 Biquad IIR Filter Megafunction
- SB 4 Complex Multiplier/Mixer Megafunction
- SB 5 Numerically Controlled Oscillator Megafunction
- SB 6 PCI Bus Target Megafunction
- SB 8 ADPCM Megafunction
- SB 9 Discrete Cosine Transform Megafunctions
- SB 10 Digital Modulator Megafunction
- SB 11 Linear Feedback Shift Register Megafunction
- SB 12 Fast Fourier Transform MegaCore Function
- SB 13 Speedbridge Megafunction
- SB 16 Convolutional Interleaver Megafunction
- SB 17 Early/Late Gate Synchronizer Megafunction

SB 18	Binary Pattern Correlator Megafunction
SB 19	EC210 PCI Bus Master/Target Megafunction
SB 20	PCI Bus Master/Target MegaCore Function
SB 21	a8259 Programmable Interrupt Controller MegaCore Function
SB 22	CAN Bus Megafunction
SB 23	Microperipheral MegaCore Library
SB 24	USB Function Controller Megafunction
SB 25	PCI Bus Target Interface Megafunction
SB 26	PCI Bus Master/Target Interface Megafunction
SB 27	RGB2YCrCb & YCrCb2RGB Color Space Converter MegaCore Functions
SB 28	USB Host Controller Megafunction
SB 30	crc MegaCore Function Parameterized CRC Generator/Checker
SB 32	Telephony Tone Generation Megafunction
SB 33	Viterbi Decoder Megafunction
SB 34	IDR Deframer Megafunction
SB 36	IEEE 1394-Compatible LLC-I Megafunction
SB 37	64-Bit PCI Bus Target Megafunction
SB 38	SDRAM Controller Megafunction
SB 39	I2C Master Interface Megafunction
SB 40	I2C Slave Interface Megafunction
SB 41	FIR Compiler MegaCore Function
SB 42	Interleaver/Deinterleaver MegaCore Function
SB 44	64-Bit PCI Master/Target MegaCore Function
SB 46	FLEX PCI Development Kit

Technical Briefs

TB 24	The Advantages of LPM
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User Guides & Software Manuals

FIR Compiler MegaCore Function User Guide
MAX+PLUS II Getting Started Manual
pci_b & pcit1 MegaCore Function User Guide
pci_c MegaCore Function User Guide
PCI MegaCore Function User Guide
Quartus Installation & Licensing for PCs

[Quartus Installation & Licensing for UNIX Workstations](#)

[Quartus SignalTap User's Guide](#)

[Quartus Tutorial](#)

[Reed-Solomon Compiler MegaCore Function User Guide](#)

[Reed-Solomon MegaCore Function User Guide](#)

[Simulating the a6402 Model with the Visual IP Software User Guide](#)

[Simulating the a8237 Model with the Visual IP Software User Guide](#)

[Simulating the a8251 Model with the Visual IP Software User Guide](#)

[Simulating the a8259 Model with the Visual IP Software User Guide](#)

[Symbol Interleaver/De-Interleaver MegaCore Function User Guide](#)